



UTP2012Z

PNP EPITAXIAL SILICON TRANSISTOR

55V PNP LOW SATURATION MEDIUM POWER TRANSISTOR

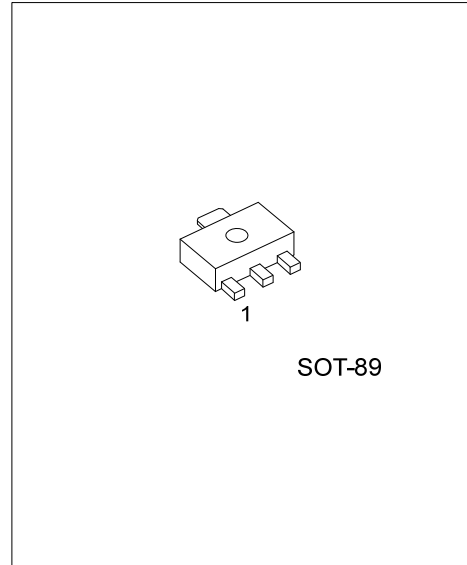
DESCRIPTION

The **UTP2012Z** is an PNP low $V_{CE(SAT)}$ Breakthrough In Small Signal (BISS) transistor in a medium power.

NPN complement: UTN2010Z.

FEATURES

- * Very low collector-emitter saturation voltage $V_{CE(SAT)}$
- * High collector current capability IC and ICM
- * High collector current gain (h_{FE}) at high IC
- * High energy efficiency due to less heat generation
- * Smaller required Printed-Circuit Board (PCB) area than for conventional transistors



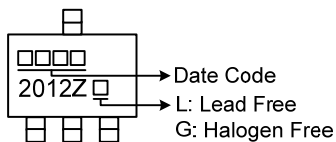
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTP2012ZL-AB3-R	UTP2012ZG-AB3-R	SOT-89	B	C	E	Tape Reel

Note: Pin Assignment: B: Base C: Collector E: Emitter

UTP2012ZG-AB3-R	(1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) AB3: SOT-89 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	V _{CBO}	-100	V
Collector to Emitter Voltage	V _{CEO}	-55	V
Emitter to Base Voltage	V _{EBO}	-7	V
Base Current	I _B	-2	A
Collector Current	I _C	-4.3	A
Peak Collector Current (t _p ≤1ms)	I _{CM}	-15	A
Collector Dissipation	P _C	1.5	W
Junction Temperature	T _J	-40 ~ +150	°C
Storage Temperature	T _{STG}	-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Single pulse, P_W=10ms.

3. Device mounted on FR-4 PCB with minimum recommended pad layout. (25×25×1.6mm)

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	83	°C/W

Note : Device mounted on FR-4 PCB with minimum recommended pad layout. (25×25×1.6mm).

■ ELECTRICAL CHARACTERISTICS (T_A= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV _{CBO}	I _C =-100μA	-100			V
Collector-Emitter Breakdown Voltage	BV _{CER}	I _C =-1μA, R _B ≤1kΩ	-100			V
Collector-Emitter Breakdown Voltage	BV _{CEO}	I _C =-10mA	-55			V
Emitter-Base Breakdown Voltage	BV _{EBO}	I _E =-100μA	-7.0			V
Collector-Base Cut-off Current	I _{CBO}	V _{CB} =-80V, I _E =0A			-20	nA
		V _{CB} =-80V, I _E =0A, T _A =100°C			-500	nA
Collector-Emitter Cut-off Current	I _{CER}	V _{CE} =-80V, R _B ≤1kΩ			-20	nA
Emitter-Base Cut-off Current	I _{EBO}	V _{EB} =-6V, I _C =0A			-10	nA
Base-Emitter On Voltage (Note)	V _{BE (ON)}	V _{CE} =-1V, I _C =-5A			-950	mV
Base-Emitter Saturation Voltage (Note)	V _{BE (SAT)}	I _C =-5A, I _B =-500mA			-1050	mV
Collector-Emitter Saturation Voltage (Note)	V _{CE(SAT)}	I _C =-100mA, I _B =-10mA			-20	mV
		I _C =-1A, I _B =-100mA			-65	mV
		I _C =-2A, I _B =-200mA			-110	mV
		I _C =-5A, I _B =-500mA			-300	mV
DC Current Transfer Ratio (Note)	h _{FE}	I _C =-10mA, V _{CE} =-1V	100			
		I _C =-2A, V _{CE} =-1V	100		300	
		I _C =-5A, V _{CE} =-1V	45			
		I _C =-10A, V _{CE} =-1V	10			
Transition Frequency (Note)	f _T	I _C =-100mA, V _{CE} =-10V, f=50MHz		120		MHz
Collector Capacitance	C _{OB}	V _{CB} =-10V, f=1MHz		90		pF
Turn-on Time	t _{ON}	I _C =-0.1A, V _{CC} =-1V, I _{B1} = I _{B2} =-10mA		280		ns
Turn-off Time	t _{OFF}			15		μs
Storage Time	t _s			14.9		μs
Fall Time	t _f			97		ns

Note: Measured under pulsed conditions. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

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