



UDN3504

Preliminary

NPN SILICON TRANSISTOR

NPN DARLINGTON POWER TRANSISTOR

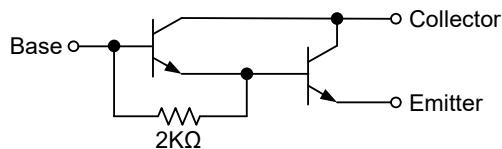
DESCRIPTION

The UTC **UDN3504** is high voltage power Darlington has been specifically designed for inductive applications such as Electronic Ignition, Switching Regulators and Motor Control.

FEATURES

- * Exceptional Safe Operating Area
- * High V_{CE} ; High Current Gain

EQUIVALENT CIRCUIT



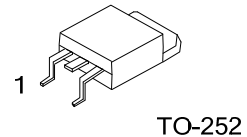
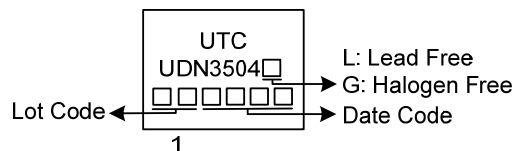
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UDN3504L-TN3-R	UDN3504G-TN3-R	TO-252	B	C	E	Tape Reel

Note: Pin Assignment: B: Base C: Collector E: Emitter

UDN3504G-TN3-R	(1) Packing Type (2) Package Type (3) Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



■ ABSOLUTE MAXIMUM RATING ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Breakdown Voltage		V_{CBO}	700	V
Collector-Emitter Sustaining Voltage		V_{CEO}	300	V
Collector-Emitter Breakdown Voltage		V_{CES}	700	V
Emitter-Base Voltage		V_{EBO}	5	V
Collector Current	DC	I_C	4	A
	Pulse(Note 2)	I_{CM}	8	A
Base Current		I_B	0.5	A
Collector Dissipation	$T_C=25^{\circ}\text{C}$	P_D	45	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-65 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse test: Pulse Width=100ms.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	75	$^{\circ}\text{C/W}$
Junction to Case	θ_{JC}	2.78	$^{\circ}\text{C/W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	I _C =100μA, I _E =0	700			V
Collector-Emitter Saturation Voltage	BV _{CEO(SUS)}	I _C =10mA	300			V
Collector Cutoff Current	I _{CES}	V _{CE} =500V, I _B =0			50	μA
Collector Cutoff Current	I _{CEO}	V _{CE} =250V, I _B =0			50	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =5V, I _C =0			5	μA
ON CHARACTERISTICS						
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C =2A, I _B =20mA			1.5	V
Base-Emitter Saturation Voltage	V _{BE(SAT)}	I _C =2A, I _B =20mA			2.0	V
Base-Emitter Turn-On Voltage	V _{BE(ON)}	I _C =2A, V _{CE} =2V			2.0	V
DC Current Gain	h _{FE}	I _C =2A, V _{CE} =2V	1000			
		I _C =2A, V _{CE} =4V	100			
DYNAMIC CHARACTERISTICS						
Current-Gain Bandwidth Product	f _T	I _C =2A, V _{CE} =10V, f=1MHz	90			MHz
Output Capacitance	C _{ob}	V _{CB} =10V, I _E =0, f=0.1MHz		60		pF

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